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NOTICE OF ALLOWANCE AND FEE(S) DUE

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05/16/2008

WENDEROTH, LIND & PONACK, L.L.P. 2033 K STREET N. W. SUITE 800 WASHINGTON, DC 20006-1021 EXAMINER

HERNANDEZ, NELSON D

ART UNIT PAPER NUMBER

2622

DATE MAILED: 05/16/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/774,466	02/10/2004	Shigetaka Kasuga	2004_0104A	9129

TITLE OF INVENTION: SOLID-STATE IMAGE SENSING APPARATUS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	08/18/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

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If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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WASHINGTON	N, DC 20006-1021							(Depositor's name)
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APPLICATION NO.	FILING DATE		FIRST NAMED INVEN	TOR	1	ATTOI	RNEY DOCKET NO.	CONFIRMATION NO.
10/774,466 ITLE OF INVENTION	02/10/2004 :: SOLID-STATE IMAC	BE SENSING APPARAT	Shigetaka Kasuga US	ı			2004_0104A	9129
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nonprovisional	NO	\$1440	\$300		\$0	\$0 \$1740		08/18/2008
EXAM	IINER	ART UNIT	CLASS-SUBCLASS					
HERNANDEZ	Z, NELSON D	2622	348-308000					
Change of correspondence address or indication of "Fee Address" (37 FR 1.363). Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.			(1) the names of u or agents OR, alter (2) the name of a s registered attorney	of a single firm (having as a member a orney or agent) and the names of up to atent attorneys or agents. If no name is				
PLEASE NOTE: Unl recordation as set fort (A) NAME OF ASSIG	less an assignee is ident h in 37 CFR 3.11. Comp GNEE		data will appear on the Tasubstitute for filing (B) RESIDENCE: (C	he pa g an a	tent. If an assignee ssignment. and STATE OR CC	OUNT	RY)	cument has been filed for up entity
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2033 K STREET N	J. W.	ART UNIT	PAPER NUMBER	
SUITE 800 WASHINGTON, DC 20006-1021			2622 DATE MAILED: 05/16/200	8

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 758 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 758 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)
	10/774,466	KASUGA ET AL.
Notice of Allowability	Examiner	Art Unit
	Nelson D. Hernández	2622
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not included will be mailed in due course. THIS
1. This communication is responsive to 3/26/2008.		
2. 🔀 The allowed claim(s) is/are <u>1-8, 13-19, and 22-25 (Renuml</u>	bered as 1-19 <u>)</u> .	
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 		
2. Certified copies of the priority documents have	been received in Application No.	
3. ☐ Copies of the certified copies of the priority do		
International Bureau (PCT Rule 17.2(a)).		3 11
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subm	IENT of this application.	
INFORMAL PATENT APPLICATION (PTO-152) which give	es reason(s) why the oath or declara	tion is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") mus		
(a) ☐ including changes required by the Notice of Draftspers	- ·	948) attached
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the C	office action of
ldentifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5.	atant Application
 Notice of References Cited (P10-692) D Notice of Draftperson's Patent Drawing Review (PTO-948) 	6. ☐ Interview Summary	, ,
3. ☐ Information Disclosure Statements (PTO/SB/08),	— Paper No./Mail Dat 7.	ė .
Paper No./Mail Date	<u>_</u>	
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 	8.	nt of Reasons for Allowance
	Supervisory Patent Examir	/Lin Ye/ er, Art Unit 2622

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 3/26/2008 has been entered.

Drawings

2. The drawings were received on February 28, 2008. These drawings are acceptable.

Response to Amendment

The Examiner acknowledges the amended claims filed on February 28,
 Claims 1 and 22-25 have been amended. Claims 9-12, 20 and 21 have been canceled.

Application/Control Number: 10/774,466

Art Unit: 2622

Allowable Subject Matter

Page 3

4. Claims 1-8, 13-19 and 22-25 (Renumbered as 1-19) are allowed.

5. The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 1, the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, that the electric charge simultaneous removal unit includes: a first switch transistor that serves as a switch; a capacitor disposed between a gate and a source or a drain of the first switch transistor; and a second switch transistor that serves as a switch, wherein the second switch transistor receives an all pixel reset switch signal and an all pixel reset signal, wherein the capacitor is charged during a time period in which the all pixel reset switch signal and the all pixel reset signal are input to a gate and a drain, respectively, of the second switch transistor, and wherein, when the capacitor is charged, the reset signal is input to the drain of the first switch transistor and is output simultaneously to all of the photoelectric conversion circuits from the source of the first switch transistor.

Regarding claim 13 (Renumbered as 11), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, an electric signal readout unit operable to read out the electric signals outputted from the photoelectric conversion circuits disposed in the region to be read out, wherein the electric signal readout unit includes: a first unit operable to output the activated reset

Application/Control Number: 10/774,466

Art Unit: 2622

signal to the each of the reset circuits in the photoelectric conversion circuits disposed in the region to be read out; and a second unit operable to output the activated readout signal to each of the reset circuits disposed in the region to be read out after outputting the reset signal, and wherein the first unit outputs the activated reset signal after the predetermined time in the electric charge accumulation unit has passed.

Regarding claim 17 (Renumbered as 15), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, an electric signal readout unit operable to read out the electric signals outputted from the photoelectric conversion circuits disposed in the region to be read out, wherein the electric signal readout unit includes: a first unit operable to output the activated reset signal to each of the reset circuits in the photoelectric conversion circuits disposed in the region to be read out; and a second unit operable to output the activated readout signal to each of the reset circuits disposed in the region to be read out after outputting the reset signal, and wherein the first unit outputs the activated reset signal before the predetermined time in the electric charge accumulation unit has passed.

Regarding claim 19 (Renumbered as 17), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, an electric signal readout unit operable to read out the electric signals outputted from the photoelectric conversion circuits disposed in the region to be read out, wherein the electric

Application/Control Number: 10/774,466

Art Unit: 2622

signal readout unit includes: a first unit operable to output the activated reset signal to each of the reset circuits in the photoelectric conversion circuits disposed in the region to be read out; and a second unit operable to output the activated readout signal to each of the reset circuits after outputting the reset signal, and wherein the first unit outputs the activated reset signal for a period starting from a mid point of the predetermined time until an end of the predetermined time in the electric charge accumulation unit.

Regarding claim 22 (Renumbered as 18), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, that the electric charge simultaneous removal unit includes: a first switch transistor that serves as a switch; a capacitor disposed between a gate and a source or a drain of the first switch transistor; and a second switch transistor that serves as a switch, wherein the second switch transistor receives an all pixel reset switch signal and an all pixel reset signal, wherein the capacitor is charged during a time period in which the all pixel reset switch signal and the all pixel reset signal are input to a gate and a drain, respectively, of the second switch transistor, and wherein, when the capacitor is charged, the reset signal is input to the drain of the first switch transistor and is output simultaneously to all of the photoelectric conversion circuits from the source of the first switch transistor.

Regarding claim 23 (Renumbered as 19), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, that wherein the electric

charge simultaneous removal unit includes: a first switch transistor that serves as a switch; a capacitor disposed between a gate and a source or a drain of the first switch transistor; and a second switch transistor that serves as a switch, wherein the second switch transistor receives an all pixel reset switch signal and an all pixel reset signal, wherein the capacitor is charged during a time period in which the all pixel reset switch signal and the all pixel reset signal are input to a gate and a drain, respectively, of the second switch transistor, and wherein, when the capacitor is charged, the reset signal is input to the drain of the first switch transistor and is output simultaneously to all of the photoelectric conversion circuits from the source of the first switch transistor.

Page 6

Contact

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nelson D. Hernández whose telephone number is (571)272-7311. The examiner can normally be reached on 9:00 A.M. to 5:30 P.M.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lin Ye can be reached on (571) 272-7372. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/774,466 Page 7

Art Unit: 2622

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nelson D. Hernández Examiner Art Unit 2622

NDHH May 10, 2008

> /Lin Ye/ Supervisory Patent Examiner, Art Unit 2622